

**Amendments to the Specification:**

Please replace the paragraph bridging pages 31 and 32 with the following amended paragraph:

As an n-channel TFT 205 in a CMOS circuit to be used for the driver circuit, a TFT having the structure suitable for reducing the injection of hot carriers so as not to decrease an operation speed is used. The driver circuit mentioned here includes a shift ~~register register~~, a buffer, a level shifter, a sampling circuit (sampling and holding circuit) or the like. A signal conversion circuit such as a D/A converter is also included therein for a digital driving.